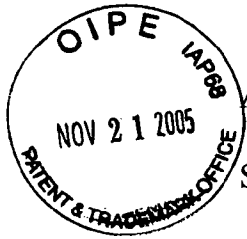


MR2833-39



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Uzi Vishkin : Art

Serial No: 10/529,310 : Unit # 2122

Filed: 25 March 2005 : Examiner:

Title: OPTICAL INTERCONNECT STRUCTURE : Unknown
 IN A COMPUTER SYSTEM AND METHOD
 OF TRANSPORTING DATA BETWEEN :
 PROCESSING ELEMENTS AND MEMORY :
 THROUGH THE OPTICAL :
 INTERCONNECT STRUCTURE

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

The Applicant wishes to make the following art references of record in the above-identified Patent Application pursuant to 37 C.F.R. §§ 1.97 and 1.98, and to the Duty of Disclosure set forth in 37 C.F.R. § 1.56

Although the information submitted herewith may be "material" to the Examiner's consideration of the subject Patent Application, this submission is not intended to constitute an admission that such information is "prior art" as to the claimed invention.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search was made or that no other material information, as defined in 37 C.F.R. § 1.56(b), exists.

MR2833-39
Serial No.: 10/529,310

The cited references are:

I. Patent references

<u>Ref. No.</u>	<u>Patent No.</u>	<u>Issue Date</u>	<u>Inventor(s)</u>
A	6,542,918	4/1/2003	Viskin
B	5,781,465	7/14/1998	Lutz, et al.
C	4,922,415	5/1/1990	Hemdel
D	4,700,325	10/13/1987	Ware

II. Other art references

<u>Reference No.</u>	<u>Description</u>
A1	Neil Savage, "Linking With Light", IEEE Spectrum, Vol. 39, Issue 8, August 2002, Pages 32-36. http://www.usc.edu/dept/engineering/eleceng/Adv_Network_Tech/Html/publications/IEEESpectrum.8.8.02.pdf
B1	D. Naishlos, et al., "Towards a First Vertical Prototyping of an Extremely Fine-Grained Parallel Programming Approach", Theory of Computer Systems, 36 (2003), 521-552 (Special Issue of SPAA2001) www.cs.umd.edu/projects/cosmic/papers/xmt-spaa01.ps
C1	U. Viskin, "Explicit Multi-Threading (XMT): A Pram-On-Chip Vision", http://www.umiacs.umd.edu/users/vishkin/XMT/

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Serial No.: 10/529,310

This Information Disclosure Statement is being filed more than three months subsequent to the filing date of the subject Patent Application, but before the mailing of a first Office Action.

A Form PTO-1449 and copies of the referenced publications are submitted along with this document. It is requested that the Examiner consider the cited references and make them of record in the above-referenced Patent Application.

Respectfully submitted,
FOR: ROSENBERG, KLEIN & LEE

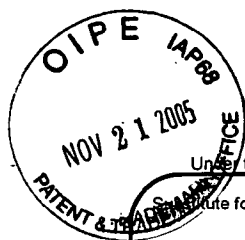


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PTO/SB/08B (04-03)

Approved for use through 04/30/2003. OMB 0651-0031

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
		Application Number	10/529,310
		Filing Date	03/25/2005
		First Named Inventor	U. Vishkin
		Art Unit	2122
		Examiner Name	
Sheet 2	of 2	Attorney Docket Number	MR2833-39

NON PATENT LITERATURE DOCUMENTS.			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A1	N. Savage, "Linking With Light", IEEE Spectrum, Vol. 39, Issue 8, August 2002, Pages 32-36. http://www.usc.edu/dept/engineering/eleceng/Adv_Network_Tech/Html/publications/IEEESpectrum.8.8.02.pdf	
	B1	D. Naishlos, et al., "Towards a First Vertical Prototyping of an Extremely Fine-Grained Parallel Programming Approach", Theory of Computer Systems, 36 (2003), 521-552 (Special Issue of SPAA2001) www.cs.umd.edu/projects/cosmic/papers/xmt-spaa01.ps	
	C1	U. Vishkin, "Explicit Multi-Threading (XMT): A Pram-On-Chip Vision", http://www.umiacs.umd.edu/users/vishkin/XMT/	

Examiner Signature	/Kidest Bahta/	Date Considered	04/19/2008
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 120 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /K.B./